

**BEST AVAILABLE COPY****ABSTRACT OF DISCLOSURE**

Disclosed is a computer system comprising: a system controller including a central processing unit and a memory bus controller and operating in a first interface mode; a system memory connected with the system controller through the system bus; a NAND flash memory for storing a system driving code, an operating system program and a user data for the computer system; and an interface unit communicating with the system controller through the system bus in the first interface mode and communicating with the NAND flash memory in a second interface mode, the interface unit being synchronized with a clock signal generated therein in response to a predetermined command information and operating. According to the invention, the NAND flash memory can be used for the system bootstrap, and data transmission to the system controller during reading or programming operation is performed successively to reduce the latency time on the read operation and the data loading time on the programming operation.